

Freeform Search

Database:	US Pre-Grant Publication Full-Text Database		
	US Patents Full-Text Database		
	US OCR Full-Text Database		
	EPO Abstracts Database		
	JPO Abstracts Database		
	Derwent World Patents Index		
	IBM Technical Disclosure Bulletins		
Term:	L17 and l3		
Display:	10	Documents in Display Format: -	Starting with Number 1
Generate: <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image			

[Search](#) [Clear](#) [Interrupt](#)

Search History

DATE: Thursday, December 09, 2004 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	<i>DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
<u>L18</u>	L17 and l3	6	<u>L18</u>
<u>L17</u>	L16 and l15	29	<u>L17</u>
<u>L16</u>	l1 adj4 (transmit or transfer or transmitting or transferring or receive or receiving or write or writing or read or reading or send or sending) adj4 (data or information)	2007	<u>L16</u>
<u>L15</u>	l2 same (access or accessing) same register	472	<u>L15</u>
<u>L14</u>	L13 and l3	72	<u>L14</u>
<u>L13</u>	(serial and interface).ti.	2451	<u>L13</u>
<u>L12</u>	(serial and interface.ti,ab.)	30020	<u>L12</u>
<u>L11</u>	l2 adj2 ("and") adj2 l1	0	<u>L11</u>
<u>L10</u>	L9 and l8	11	<u>L10</u>
<u>L9</u>	l4 adj4 l1	1644	<u>L9</u>
<u>L8</u>	l3 adj4 l1	351	<u>L8</u>
<u>L7</u>	L6 and l5	5	<u>L7</u>
<u>L6</u>	710/???.ccls.	11043	<u>L6</u>

<u>L5</u>	l1 same l2 same l3 same l4	150	<u>L5</u>
<u>L4</u>	Host or cpu or motherboard	703199	<u>L4</u>
<u>L3</u>	network adj2 (interface or card)	68161	<u>L3</u>
<u>L2</u>	parallel adj2 (interface or port)	27845	<u>L2</u>
<u>L1</u>	serial adj2 (interface or port)	48875	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Term	Documents
EMBED	57635
EMBEDS	8880
EMBEDDING	70238
EMBEDDINGS	165
ENCODE	151668
ENCODES	106600
ENCODING	278228
ENCODINGS	4237
ENCAPSULATE	46106
ENCAPSULATES	21094
ENCAPSULATING	66713
((EMBED OR EMBEDDING OR ENCODE OR ENCODING OR ENCAPSULATE OR ENCAPSULATING) SAME L7 SAME (WRITE)).PGPB,USPT,EPAB,JPAB,DWPI,TDBD.	248

There are more results than shown above. Click here to view the entire set.

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Search:

L10

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Thursday, December 09, 2004 [Printable Copy](#) [Create Case](#)

Set
Name
side by

Query

Hit
Count

Set
Name
result

side

set

DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

<u>L10</u>	(embed or embedding or encode or encoding or encapsulate or encapsulating) same l7 same (write)	248	<u>L10</u>
<u>L9</u>	(embed or embedding or encode or encoding or encapsulate or encapsulating) same l7 same (write adj2 command)	2	<u>L9</u>
<u>L8</u>	(embed or embedding or encode or encoding or encapsulate or encapsulating) adj4 l7 adj4 (write adj2 command)	0	<u>L8</u>
<u>L7</u>	(data adj2 structure)	87781	<u>L7</u>
<u>L6</u>	L5 and l4	9	<u>L6</u>
<u>L5</u>	serial adj2 (link or port or interface)	54764	<u>L5</u>
<u>L4</u>	(encode or encapsulate or encapsulating or encoding or embed or embedding) adj4 (write adj2 command)	21	<u>L4</u>
<u>L3</u>	(encode or encapsulate or encapsulating or encoding or embed or embedding) same (write adj2 command) same (format or formatting)	21	<u>L3</u>
<u>L2</u>	host and l1	1	<u>L2</u>
<u>L1</u>	6275501.pn.	2	<u>L1</u>

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	IBM Technical Disclosure Bulletins
Term:	controller adj4 (implement or implementing) adj4
	ASIC
Display:	<input type="text" value="10"/> Documents in Display Format: <input type="text" value="-"/> Starting with Number <input type="text" value="1"/>
Generate: <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image	

Search History

DATE: Thursday, December 09, 2004 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

Hit Count Set Name

result set

DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

<u>L5</u>	controller adj4 (implement or implementing) adj4 ASIC	3	<u>L5</u>
<u>L4</u>	controller adj4 (implement or implementing) adj4 FPGA	0	<u>L4</u>
<u>L3</u>	(controller adj4 FPGA) same advantages	19	<u>L3</u>
<u>L2</u>	controller adj4 FPGA	482	<u>L2</u>
<u>L1</u>	controller same FPGA	3464	<u>L1</u>

END OF SEARCH HISTORY